

Amendments to the Claims:

Claims 1 and 13 have been amended herein. Please note that all claims currently pending and under consideration in the referenced application are shown below. Please enter these claims as amended. This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims:

1. (Currently Amended) An apparatus for assisting in compressing video data in a computer system including a central processing unit and a system memory, comprising:
a video input port configured to electrically couple to a video unit for receiving video data for a current video frame from streaming video data;
a video input buffer coupled to the video input port for storing the video data from the video input port;
a previous frame buffer for storing at least a portion of a previous video frame;
an operation unit coupled to the video input buffer and the previous frame buffer for computing a difference frame from data from the video input buffer and data from the previous frame buffer; and
a result buffer coupled to the operation unit, the result buffer further including a memory interface configured to couple with the system memory via a first bus independent of a second bus configured for coupling with the central processing unit via a north bridge chip, the result buffer for temporarily buffering the difference frame prior to storing the difference frame in the system memory, the apparatus configured to operate within the [[a]] north bridge chip of the computer system to enable the central processing unit to retrieve the difference frame exclusive of the second bus and directly from the system memory via the north bridge chip for further compression of the video data by the central processing unit.

2. (Previously presented) The apparatus of claim 1, including a memory port coupled to the previous frame buffer and the result buffer for transferring data to and from the system memory that stores video data from the video input port and result data from the result buffer.

3. (Previously presented) The apparatus of claim 2, wherein the system memory couples to the memory port for storing the video data from the video input port and the difference frame from the result buffer, the video data is stored to in a current frame area in the system memory and the difference frame is stored in a difference frame area in the memory.

4. (Previously Presented) The apparatus of claim 3, wherein the memory stores a current video frame and a previous video frame in the same location in the system memory, allowing the current video frame to be written over the previous video frame.

5. (Previously Presented) The apparatus of claim 3, wherein the system memory also stores instructions and data for the central processing unit of the computer system.

6. (Original) The apparatus of claim 1, wherein the operation unit performs an exclusive-OR operation between data from the video input buffer and data from the previous frame buffer.

7. (Original) The apparatus of claim 1, wherein:
the video input buffer stores a block of data from the video input port;
the previous frame buffer stores a block of data from the previous video frame;
the result buffer stores a block of data from the operation unit; and
the operation unit performs an operation between a block of data from the video input port and a block of data from the previous frame buffer.

8. (Canceled)
9. (Original) The apparatus of claim 1, wherein the apparatus comprises part of a video conferencing system.
10. (Previously presented) The apparatus of claim 1, including additional resources within the apparatus for compressing the video data from the video input port.
11. (Original) The apparatus of claim 1, including a color space conversion circuit coupled between the video input port and the video input buffer.
12. (Original) The apparatus of claim 1, wherein the video input buffer is a register that stores less than one video frame.

13. (Currently Amended) An apparatus for compressing video data in a computer system including a central processing unit, comprising:

- a video input port configured to electrically couple to a video unit for receiving video data for a current video frame from streaming video data;
- a video input buffer coupled to the video input port for storing the video data from the video input port;
- a previous frame buffer for storing a least a portion of a previous video frame;
- an exclusive-OR unit coupled to the video input buffer and the previous frame buffer for computing a difference frame from data from the video input buffer and data from the previous frame buffer;
- a result buffer coupled to the exclusive-OR unit for temporarily buffering the difference frame;
- a memory port coupled to the previous frame buffer and the result buffer, the memory port independent from the video input port; and
- a system memory coupled to the memory port for storing the video data from the video input port and the difference frame from the result buffer, wherein the video data is stored to in a current frame in the memory, the apparatus configured to operate within a north bridge chip of the computer system to enable the central processing unit via the north bridge chip to retrieve the difference frame exclusive of a system bus directly from the system memory via the north bridge chip for further compression of the video data by the central processing unit.

14. (Previously presented) The apparatus of claim 13, wherein the system memory stores a current video frame and a previous video frame in the same location allowing the current video frame to be written over the previous video frame.

15. (Previously Presented) The apparatus of claim 13, wherein the system memory stores instructions and data for the central processing unit of the computer system.

16. (Previously Presented) The apparatus of claim 13, wherein:
the video input buffer stores a block of data from the video input port;
the previous frame buffer stores a block of data from the previous video frame;
the result buffer stores a block of data from the exclusive-OR unit; and
the exclusive-OR unit performs an exclusive-OR operation between a block of data from the
video input port and a block of data from the previous frame buffer.
17. (Canceled)
18. (Original) The apparatus of claim 13, wherein the apparatus comprises part of a
video conferencing system.
19. (Original) The apparatus of claim 13, including a color space conversion circuit
coupled between the video input port and the video input buffer.